KANEKO.007AUS PATENT

ABSTRACT OF THE DISCLOSURE

A process of manufacturing a liquid crystal display device of transverse electric-field type, where a halftone photomask has a fully light-shielding area preventing UV irradiation of a portion of an active matrix substrate in which a thin-film transistor element is to be formed, so that the photoresist pattern includes a positive resist portion which is formed on the portion of the substrate. The halftone mask further has a fully light-transmitting area which permits full UV transmission therethrough to provide the photoresist pattern with a resist-free area which corresponds to a portion of the substrate in which a contact hole serving as a third connection portion connecting an external scanning-line driver circuit and a scanning-line terminal portion through a junction electrode is to be formed. Also disclosed is a scan-exposing device used in the manufacturing process.

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